

# **VACANCY**

Position: Senior Engineer - Digital Design

Business unit: GEW Location: Pretoria

HENSOLDT South Africa is looking for a Senior Engineer in Digital Design to form part of our Engineering team to develop the high-tech product portfolio of its GEW business unit in Pretoria as soon as possible.

HENSOLDT is a global high-tech pioneer in the sector of defence and security electronics and a market leader in civilian and military sensor solutions. HENSOLDT South Africa is part of the HENSOLDT Group. HENSOLDT SA GEW, formerly GEW Technologies, is a world leader in the development, manufacture and supply of Signal Intelligence, Spectrum Management and Intelligent Security Solutions.

#### Job overview

The purpose of Senior Engineer Digital Design is to form part of a technical team in the design and development, assembly, testing and verification of hardware products. This may entail the engineering disciplines of Radio Frequency and Antennas, Digital, Firmware and Embedded SW, Mixed Signal (incl Power Supply) and Signal Processing Software. Personal growth and development will lead to taking full responsibility as design authority for a specific engineering discipline or Product Owner/Manager responsibility for a specific product line.

# General areas of responsibility

- FPGA logic design and simulation using VHDL
- Development of embedded C/C++ software for controller applications
- Interfacing embedded software to custom hardware components
- Develop high-level PC software components for interfacing with embedded systems
- Develop, test, and document software and firmware for a variety of microprocessors and programmable logic devices
- Generation of development and interface documentation

### Personal attributes and skills required

- Must be a creative thinker that can work in a disciplined project environment
- Motivated, self-starting and driven
- Must have good communication and interpersonal skills
- · Must be focused on quality
- Must be able to work independently and in a team

### Qualifications and experience required

- BEng (Electronics, Computer)
- At least 5 years relevant experience
- C/C++ and VHDL knowledge and experience required
- Knowledge of embedded operating systems (Embedded Linux, RTOS)
- Knowledge of embedded hardware interfacing protocols (UART, SPI, etc.)
- Experience with DSP and ARM processors will be an advantage
- Experience in a multi-disciplinary engineering R & D environment will be an advantage
- · Strong mathematical and science background required
- Digital Signal Processing knowledge is essential



• Electronic Warfare domain experience will be an advantage

Interested candidates may apply by submitting their resumes via e-mail, with reference to the position in the subject line.

## Applications can be sent to Cymondi Stassen at recruitment@hensoldt.net

Should you not be contacted within 14 days after the closing date, please regard your application as unsuccessful.

# **About HENSOLDT South Africa**

HENSOLDT South Africa is a pioneer of technology and innovation in defence and security electronics. The company offers a comprehensive range of products, systems and services across defence and civil markets, from spectrum dominance, optronics and radar, to IFF, datalinks and integrated security solutions. With more than 800 South African employees across three sites in South Africa, it is the HENSOLDT Group's largest industrial base outside of Europe and one of the largest defence and security electronics companies in South Africa.

HENSOLDT South Africa focuses on diversity, equity and inclusion in our organisational strategy. We are committed to building a diverse and inclusive corporate culture to the benefit of our employees and to deliver better outcomes for our clients. In line with our commitment to employment equity and our focus on diversity in the operational environment, we welcome applications from all ethnic groups. In line with HENSOLDT's commitment to employment equity and our focus on diversity in the operational environment, preference will be given to suitable candidates from designated groups.